

# NEWS

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## **NOVELICS MOVES HEADQUARTERS TO NEW LOCATION IN ALISO VIEJO, CALIF.**

### *Embedded Memory Company's Growth Triggers Office Expansion*

**ALISO VIEJO, Calif., Dec. 19, 2007** – Novelics, a leading provider of semiconductor embedded memory circuit Intellectual Property (IP), today announced it has moved its headquarters to a larger building in Aliso Viejo, Calif. to accommodate new employees to handle its growing customer base and rapidly expanding line of embedded memory products. Novelics' offices are now located at 85 Enterprise, Suite 350.

Novelics provides custom semiconductor memory circuits that offer industry-leading solutions for reducing power consumption and the silicon area of semiconductor products while simultaneously increasing the overall performance, reliability and security of the resulting system. Novelics' products include a variety of memory formats, including SRAM-6T and SRAM-1T, high-speed cache, ROM and OTP. The Novelics memory circuit products are being integrated into designs by many of the largest and most innovative semiconductor companies in the world, paving the way for new consumer electronics products that are lighter, less expensive, more powerful and have longer battery life.

"We've rapidly outgrown our current headquarters in Aliso Viejo and have moved into a facility twice as large, which will enable us to hire additional employees as we continue to expand our product availability and meet our customer requirements," said Cyrus Afghahi, Ph.D., CEO and co-founder of Novelics. "We are gratified by the response from the semiconductor community to our products and are looking forward to additional growth in 2008."

The new office space is approximately 8,000 square feet. The company also has sales associates across the globe and plans to open an international design center in 2008. Novelics can continue to be reached at its current telephone number, 949-448-5900.

For more information on the company, visit <http://www.novelics.com>.

**About Novelics**

Novelics, headquartered in Aliso Viejo, Calif., supplies a portfolio of innovative embedded memory IPs for low-power and high-performance ASIC, ASSP, and SoC designs. Novelics' compiler-driven "cool" and "zero-leakage" Memory IPs include SRAM-1T, SRAM- 6T, OTP, high-speed Cache, CAM, and ROM. These differentiated memory IPs are implemented with the standard logic CMOS process with no additional masks or process steps to minimize cost and to maximize reliability and portability. Novelics' customers compete in low-power consumer, wireless, high-speed computing, industrial, and networking applications. For more information, please visit [www.novelics.com](http://www.novelics.com).

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